

## FEATURES

- High Speed Data Connection between Concept/32 RMS and PCI-RMS Nodes
- Bridges to either RMS I Bus or RMS II Bus
- Controlled by Configurable Memory Regions
- Provides RMS Bus (I or II) Arbitration and Termination

## BENEFITS

- Uses Existing RMS Bus Cables
- No Additional Software Required
- Connects Legacy Encore Computer Systems to Modern "Open" Computer Systems
- Available for Linux, Windows XP and NT, HP Tru64, OpenVMS, and Solaris

# PCI-RMS Bridge

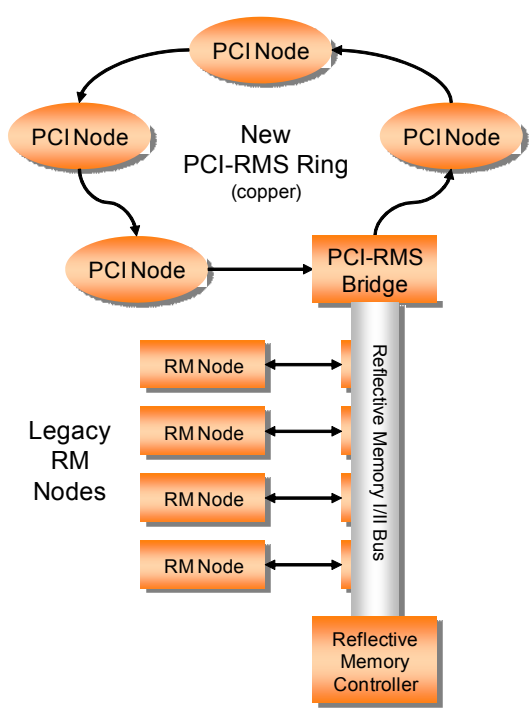
## Overview

The PCI-RMS Bridge provides a path between Compro's legacy Reflective Memory System (RMS) and the Compro's PCI-RMS Ring. The Bridge allows either RMS I or RMS II transfers to propagate to PCI-RMS Nodes and PCI-RMS Node transfers to flow onto the RMS I or RMS II Bus.

The Bridge provides a method for connecting nodes to an existing RMS Bus using "state-of-the-art" computing systems. This capability offers a seamless, non-intrusive mechanism to augment legacy systems with powerful new features using today's computing solutions. By adding PCI-RMS nodes, the architecture can easily expand into a tightly-coupled "super-system," cooperatively working on the most challenging applications.

The Bridge requires one node ID on the RMS Bus (I or II) and one node ID on the PCI-RMS Ring. PCI-RMS Ring transfers below 16 MB/sec can be selected to pass to an RMS I Bus. PCI-RMS Ring transfers below 256 MB/sec can be selected to pass to an RMS II Bus

The following block diagram illustrates how a PCI-RMS Ring connects to a Concept RMS Bus using the RMS Bus to PCI-RMS Bridge.





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## *Physical Description*

The Bridge has a similar form factor to a Reflective Memory Controller (RMC). During installation, an RMC is removed from the RMC Chassis and the Bridge is installed in its place. Compatible RMS Bus cable connectors are provided for connection to the RMS Bus. Two copper PCI-RMS cable connectors are provide for connection PCI-RMS Ring nodes.

